

TABLE I. Electrical performance characteristics – Continued.

Test	Symbol	Conditions 1/ 2/ -55°C ≤ TA ≤ +125°C unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
$V_S = +3 \text{ V}$ , $V_{CM} = 1.5 \text{ V}$ section							
Input offset voltage 4/	VOS		1	01,02		200	$\mu\text{V}$
			2, 3			400	
Input offset current 4/	IOS	3/	1, 2, 3	01,02		50	nA
Input bias current 4/	IB	3/	1	01,02		350	nA
			2, 3			575	
Common-mode 4/ rejection ratio	CMRR	$V_{CM} = 0 \text{ V}$ to $3 \text{ V}$	1	01,02	60		dB
			2, 3		56		
Output high voltage 4/	VOH	$I_L = 1 \text{ mA}$	4	01,02	2.80		V
			5, 6		2.65		
Output low voltage 4/	VOL	$I_L = 1 \text{ mA}$	4	01,02		125	mV
			5, 6			200	
Supply current 4/ 5/	ISY	$V_{OUT} = 1.5 \text{ V}$	1	01,02		5.4	mA
$V_S = \pm 15 \text{ V}$ , $V_{CM} = 0 \text{ V}$ section							
Input offset voltage 4/	VOS		1	01,02		250	$\mu\text{V}$
			2, 3			500	
Average input offset 4/ voltage	TCVOS		8	01,02		2.5	$\mu\text{V}/^\circ\text{C}$
Input offset current 4/	IOS		1, 2, 3	01,02		50	nA
Input bias current 4/	IB		1	01,02		350	nA
			2, 3			575	
Common-mode 4/ rejection ratio	CMRR	$V_{CM} = -15 \text{ V}$ to $+15 \text{ V}$	1, 2, 3	01,02	80		dB
Power supply rejection 4/ ratio	PSRR	$V_S = \pm 2 \text{ V}$ to $\pm 18 \text{ V}$	1, 2, 3	01,02	90		dB
Output high voltage 4/	VOH	$I_L = 1 \text{ mA}$	4	01,02	14.8		V
			5, 6		14.7		
Output low voltage 4/	VOL	$I_L = 1 \text{ mA}$	4	01,02		-14.875	V
			5, 6			-14.8	

See footnotes at end of table.

<b>STANDARD MICROCIRCUIT DRAWING</b> DLA LAND AND MARITIME COLUMBUS, OHIO 43218-3990	SIZE <b>A</b>	REVISION LEVEL <b>J</b>	<b>5962-00517</b>